

Features

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent Cdv/dt effect decline
- ★ Advanced high cell density Trench technology

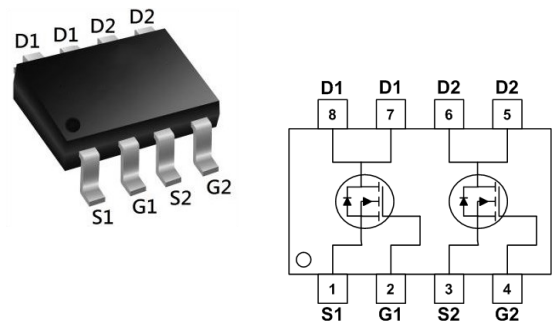
Description

The RJ9926A uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Product Summary

$V_{DS}=20V$
 $I_D=6A$ ($V_{GS}=4.5V$)
 $R_{DS(ON)}<26m\Omega$ ($V_{GS}=4.5V$) Typ= $20m\Omega$
 $R_{DS(ON)}<32m\Omega$ ($V_{GS}=2.5V$) Typ= $25m\Omega$

Dual SOP8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D@T_A=25^\circ C$	Continuous Drain Current, V_{GS} @ 4.5V ¹	6.0	A
$I_D@T_A=70^\circ C$	Continuous Drain Current, V_{GS} @ 4.5V ¹	4.8	A
I_{DM}	Pulsed Drain Current ²	25	A
$P_D@T_A=25^\circ C$	Total Power Dissipation ³	1.65	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	78	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	--	-	$^\circ C/W$

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.4	0.7	1.0	V
R _{DS(on)}	Static Drain-Source on-Resistance note2	V _{GS} =4.5V, I _D =6A	-	20	26	mΩ
		V _{GS} =2.5V, I _D =5A	-	25	32	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1.0MHz	-	358	-	pF
C _{oss}	Output Capacitance		-	69.3	-	pF
C _{rss}	Reverse Transfer Capacitance		-	58.5	-	pF
Q _g	Total Gate Charge	V _{DS} =10V, I _D =3A, V _{GS} =4.5V	-	5.6	-	nC
Q _{gs}	Gate-Source Charge		-	0.8	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	1	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =10V, I _D =6A, R _{GEN} =3Ω, V _{GS} =4.5V	-	16	-	ns
t _r	Turn-on Rise Time		-	51	-	ns
t _{d(off)}	Turn-off Delay Time		-	21	-	ns
t _f	Turn-off Fall Time		-	19	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	6	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	24	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =6A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Performance Characteristics

Figure 1: Output Characteristics

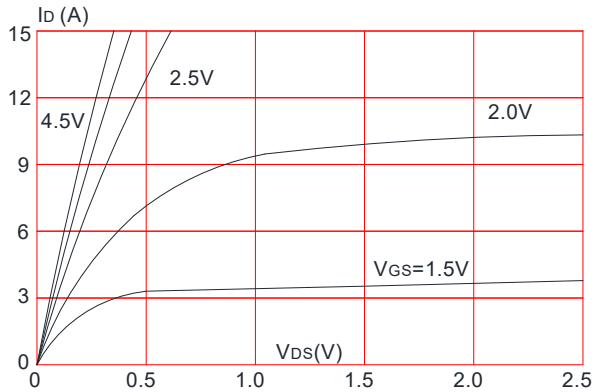


Figure 2: Typical Transfer Characteristics

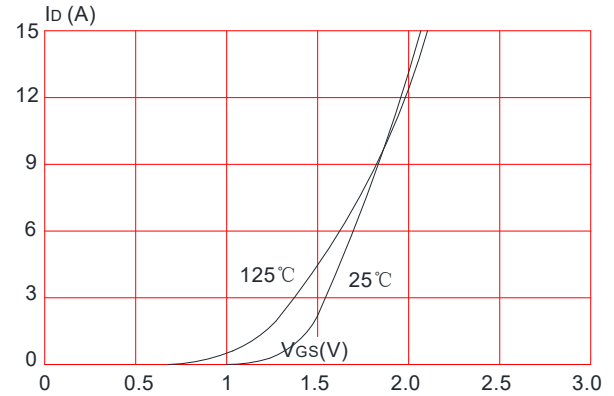


Figure 3: On-resistance vs. Drain Current

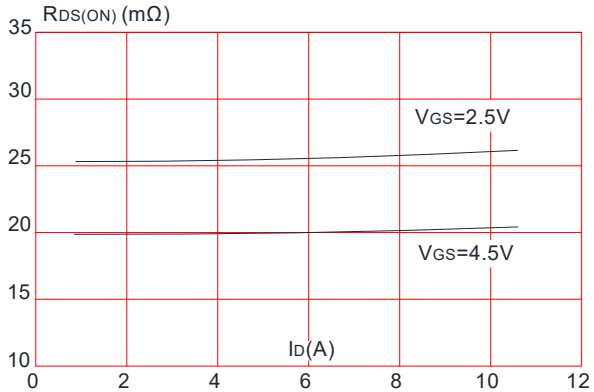


Figure 4: Body Diode Characteristics

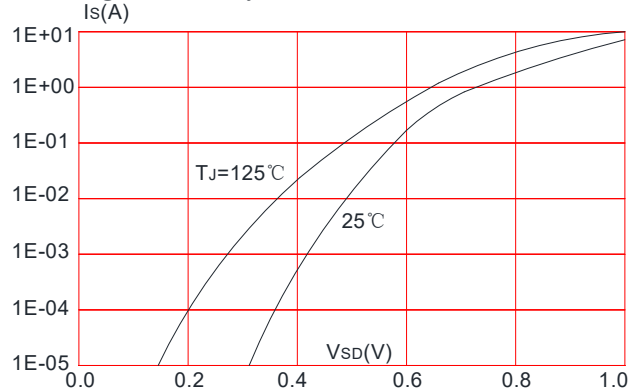


Figure 5: Gate Charge Characteristics

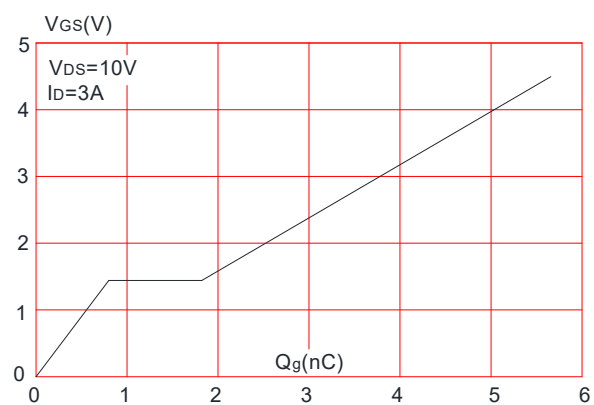


Figure 6: Capacitance Characteristics

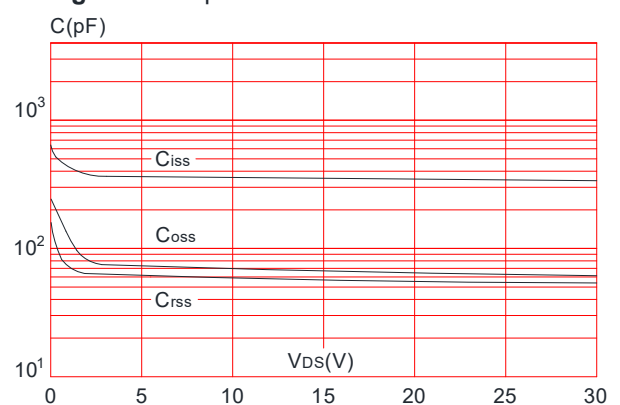


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

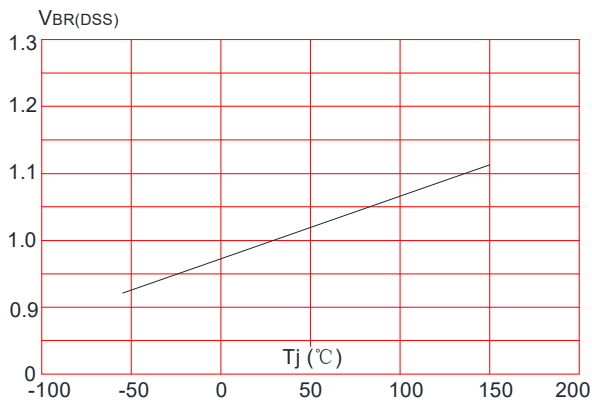


Figure 8: Normalized on Resistance vs. Junction Temperature

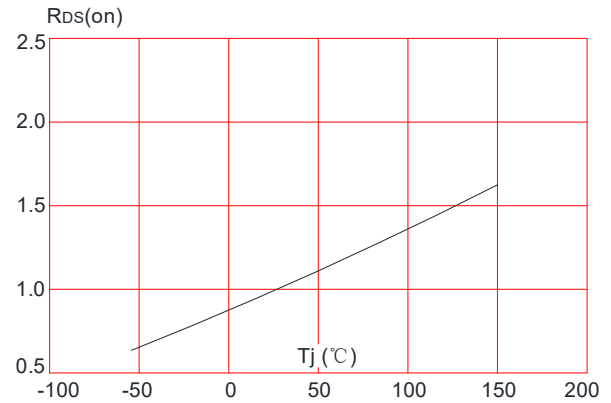


Figure 9: Maximum Safe Operating Area

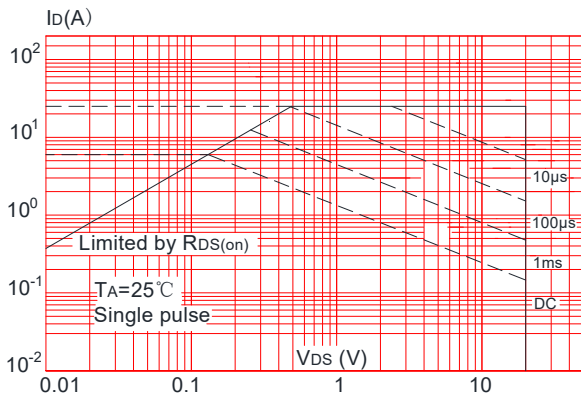


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

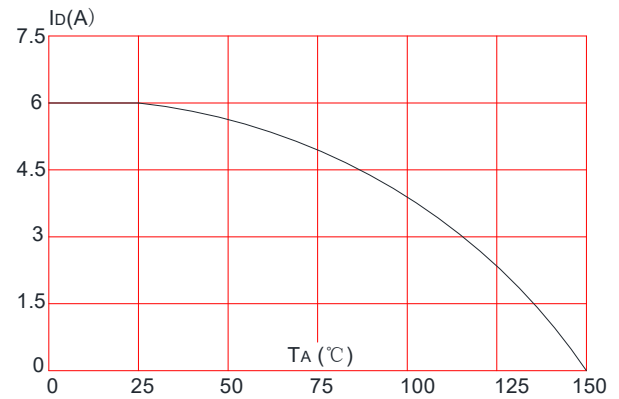
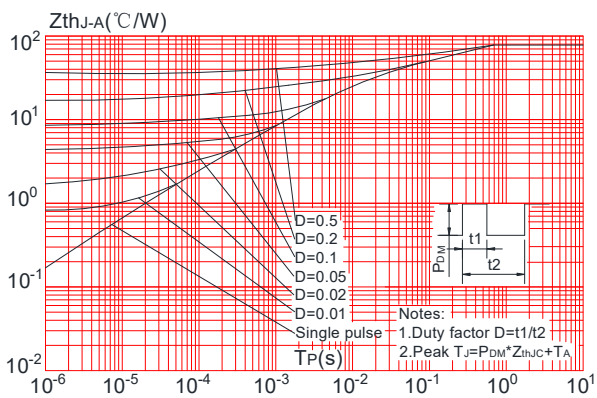
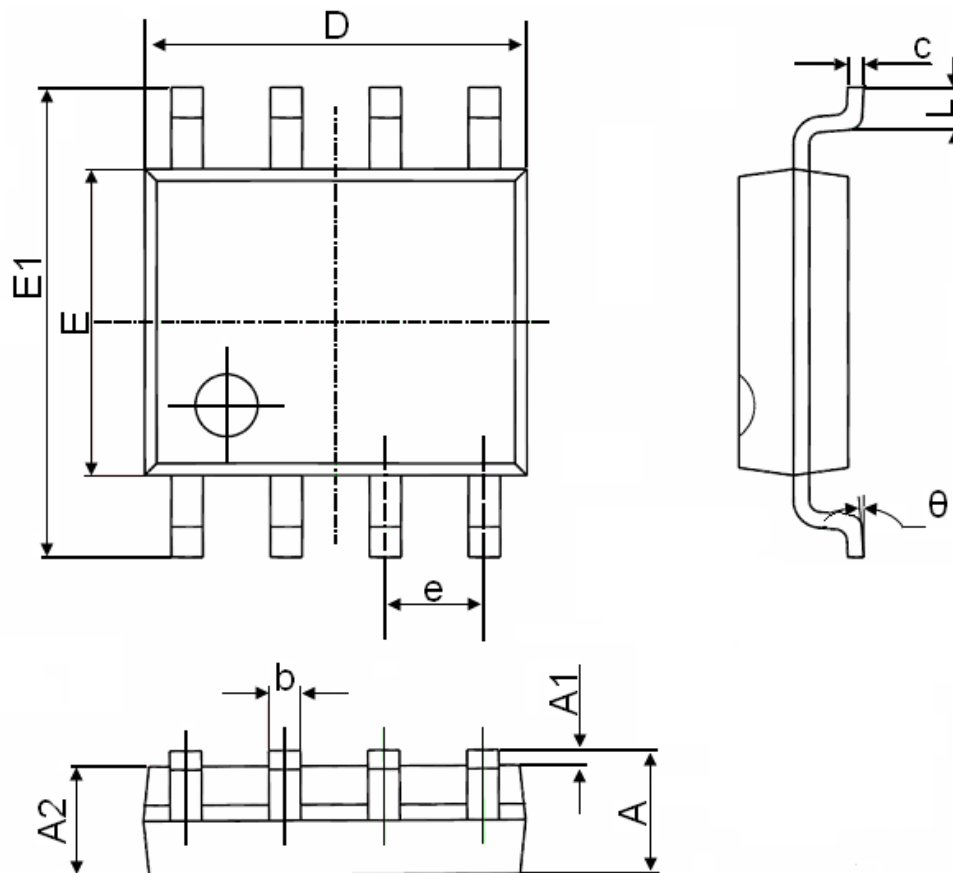


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Package Mechanical Data-SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°