

General Description

The 7800 series are monolithic integrated circuits designed as fixed-voltage regulators for a wide variety of applications including local, on-card regulation.

This series of regulators are complete with internal current limiting, thermal shutdown protection, and safe-area compensation which make them virtually immune from output overload. If adequate heat sinking are provided, these regulators can deliver output currents up to 1.0A.

The 7800 series are available in two standard plastic packages: TO-220-3 and TO-252-2.

Features

- Constant Output Current up to 1.0A
 - Fixed Output Voltages of 5V, 6V, 8V, 9V and 12V
 - Output Voltage Tolerances of $\pm 5\%$ over the Full Temperature Range
 - Internal Short Circuit Current-limiting
 - Internal Thermal Overload Protection

Applications

- Consumer Electronics
 - Microprocessor Power Supply
 - Mother Board I/O Power Supply

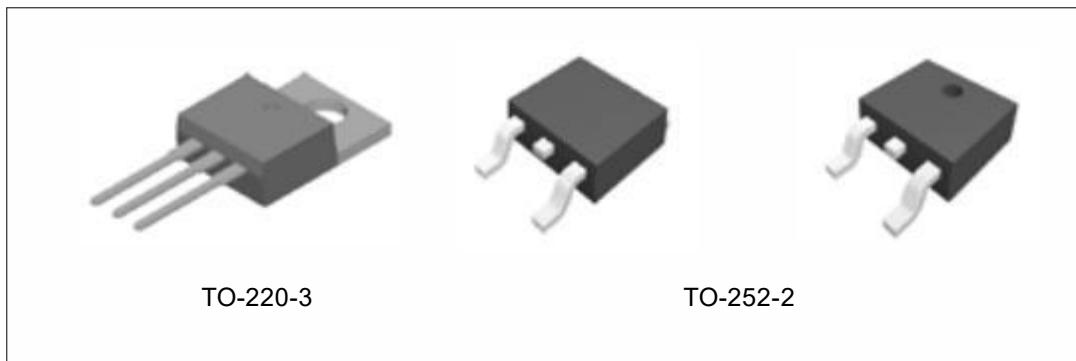
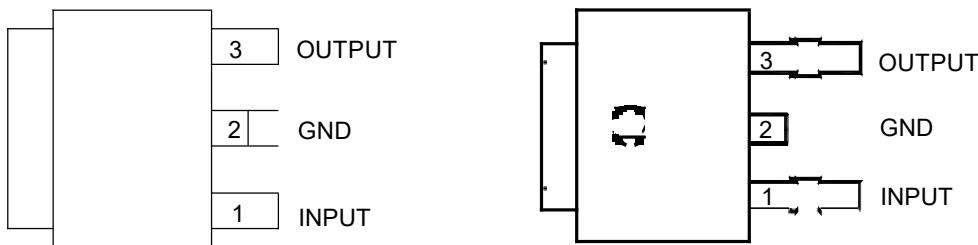


Figure 1. Package Types of 7800

1A3-TERMINAL POSITIVE LINEAR REGULATOR
7800
Pin Configuration

D Package
(TO-252-2)



T Package
(TO-220-3)

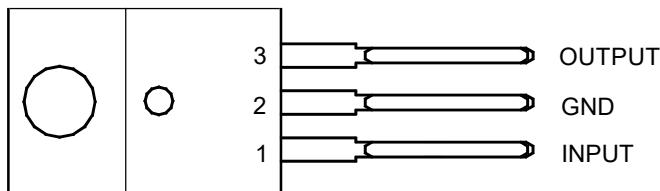


Figure 2. Pin Configuration of 7800 (Top View)

Pin Description

Pin Number	Pin Name	Function
1	INPUT	Voltage Input
2	GND	Ground
3	OUTPUT	Voltage Output

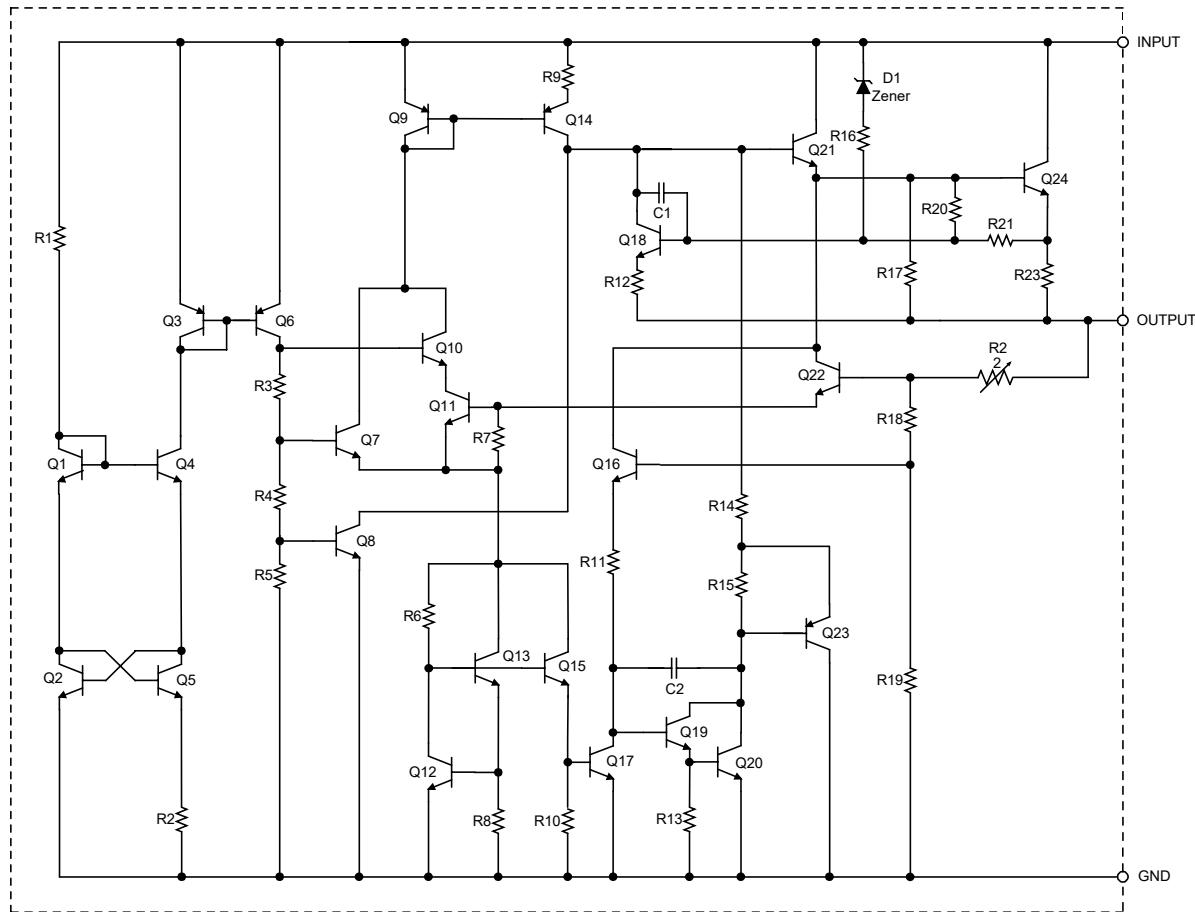
1A3-TERMINAL POSITIVE LINEAR REGULATOR
7800
Functional Block Diagram


Figure 3. Functional Block Diagram of 7800

1A3-Terminal Positive Linear Regulator
7800
Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Value		Unit
Input Voltage	V _{IN}	35		V
Lead Temperature (Soldering, 10sec)	T _{LEAD}	260		°C
Power Dissipation	P _D	Internally Limited		W
Operating Junction Temperature	T _J	150		°C
Storage Temperature Range	T _{STG}	-65 to 150		°C
Thermal Resistance	θ _{JA}	TO-220-3	60	°C/W
		TO-252-2	100	
ESD (Human Body Model)	ESD	2500		V
ESD (Machine Model)	ESD	2500		V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Input Voltage	V _{IN}		28	V
			28	
			28	
			28	
			30	
Operating Junction Temperature Range	T _J	-40	125	°C

1A3-TERMINAL POSITIVE LINEAR REGULATOR
7800
Electrical Characteristics

7805 (Vi=10V, Io=500mA, TJ=-40 to 125 °C, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	VO	TJ=25°C	4.8	5.0	5.2	V
		Io=5mA to 1A, Vi=7 to 20V, PD ≤ 15W	4.75	5.0	5.25	
Line Regulation	VRLINE	Vi=7V to 25V, Io=1A, TJ=25°C		5	100	mV
Load Regulation	VRLOAD	Vi=10V, Io=5mA to 1.5A, TJ=25°C		5	100	mV
Quiescent Current	I _Q	Vi=10V		3.2	6.0	mA
Quiescent Current Change	ΔI _Q	Vi=7V to 25V, Io=500mA, TJ=25°C		0.3	1.1	mA
		Io=5mA to 1A, TJ=25°C		0.08	0.5	
Ripple Rejection	ΔVi/ΔVO	Vi=8V to 18V, f=120Hz	62	73		dB
Dropout Voltage	V _{DROP}	ΔVo/Vo=1%, Io=1A, TJ=25°C		2.0		V
Output Noise Voltage	No	f=10Hz to 100KHz, TA=25°C		10		μV/Vo
Output Resistance	R _O	f=1.0kHz		10		mΩ
Short Circuit Current	ISC	Vi=35V, TA=25°C		0.2		A
Peak Output Current	IPK	Vi=10V, TJ=25°C		2.2		A
Output Voltage Drift	ΔVo/ΔT			0.3		mV/°C
Thermal Resistance	θ _{JC}	TO-220-3		8.8		°C/W
		TO-252-2		15.7		

7806 (Vi=11V, Io=500mA, TJ=-40 to 125 °C, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	VO	TJ=25°C	5.75	6.0	6.25	V
		Io=5mA to 1A, Vi=8 to 21V, PD ≤ 15W	5.7	6.0	6.3	
Line Regulation	VRLINE	Vi=8V to 25V, TJ=25°C		6	120	mV
Load Regulation	VRLOAD	Vi=11V, Io=5mA to 1.5A, TJ=25°C		2	120	mV
Quiescent Current	I _Q	Vi=11V		3.3	6.0	mA
Quiescent Current Change	ΔI _Q	Vi=8V to 25V, Io=500mA, TJ=25°C		0.3	1.3	mA
		Io=5.0mA to 1A, TJ=25°C		0.08	0.5	
Ripple Rejection	ΔVi/ΔVO	Vi=9V to 19V, f=120Hz	58	65		dB
Dropout Voltage	V _{DROP}	ΔVo/Vo=1%, Io=1A, TJ=25°C		2.0		V
Output Noise Voltage	No	f=10Hz to 100KHz, TA=25°C		10		μV/Vo
Output Resistance	R _O	f=1.0kHz		10		mΩ
Short Circuit Current	ISC	Vi=35V, TA=25°C		0.2		A
Peak Output Current	IPK	Vi=11V, TJ=25°C		2.2		A
Output Voltage Drift	ΔVo/ΔT			0.3		mV/°C
Thermal Resistance	θ _{JC}	TO-220-3		8.8		°C/W
		TO-252-2		15.7		

1A3-TERMINAL POSITIVE LINEAR REGULATOR

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Electrical Characteristics (Continued)

7808 (VI=14V, IO=500mA, TJ=-40 to 125 °C, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	VO	TJ=25°C	7.7	8.0	8.3	V
		IO=5mA to 1A, VI=10.5 to 23V, PD ≤ 15W	7.6	8.0	8.4	
Line Regulation	VRLINE	VI=10.5V to 25V, IO=1A, TJ=25°		6	160	mV
Load Regulation	VRLOAD	VI=14V, IO=5mA to 1.5A, TJ=25°C		1.4	160	mV
Quiescent Current	I _Q	VI=14V		3.3	6.0	mA
Quiescent Current Change	ΔI _Q	VI=10.5V to 25V, IO=500mA, TJ=25°		1	0.5	mA
		IO=5.0mA to 1A, TJ=25°C				
Ripple Rejection	ΔVI/ΔVO	VI=11.5V to 18V, f=120Hz	56	62		dB
Dropout Voltage	V _{DROP}	ΔVo/Vo=1%, IO=1A, TJ=25°C		2.0		V
Output Noise Voltage	NO	f=10Hz to 100KHz, TA=25°C		10		μV/Vo
Output Resistance	R _O	f=1.0kHz		10		mΩ
Short Circuit Current	I _{SC}	VI=35V, TA=25°C		0.2		A
Peak Output Current	I _{PK}	VI=13V, TJ=25°C		2.2		A
Output Voltage Drift	ΔVo/ΔT			0.4		mV/°C
Thermal Resistance	θ _{JC}	TO-220-3		8.8	15.7	°C/W
		TO-252-2				

7809 (VI=15V, IO=500mA, TJ=-40 to 125 °C, unless otherwise specified.)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Output Voltage	VO	TJ=25°C	8.65	9.0	9.35	V
		IO=5mA to 1A, VI=11.5 to 24V, PD ≤ 15W	8.55	9.0	9.45	
Line Regulation	VRLINE	VI=11.5V to 27V, IO=1A, TJ=25°C		6	180	mV
Load Regulation	VRLOAD	VI=15V, IO=5mA to 1.5A, TJ=25°C		2	180	mV
Quiescent Current	I _Q	VI=15V		3.4	8.0	mA
Quiescent Current Change	ΔI _Q	VI=11.5V to 27V, IO=500mA, TJ=25°C		1.0	0.5	mA
		IO=5.0mA to 1A, TJ=25°C				
Ripple Rejection	ΔVI/ΔVO	VI=11.5V to 21.5V, f=120Hz	56	61		dB
Dropout Voltage	V _{DROP}	ΔVo/Vo=1%, IO=1A		2.0		V
Output Noise Voltage	NO	f=10Hz to 100KHz, TA=25°C		10		μV/Vo
Output Resistance	R _O	f=1.0kHz		12		mΩ
Short Circuit Current	I _{SC}	VI=35V, TA=25°C		0.2		A
Peak Output Current	I _{PK}	VI=14V, TJ=25°C		2.2		A
Output Voltage Drift	ΔVo/ΔT			0.5		mV/°C
Thermal Resistance	θ _{JC}	TO-220-3		8.8	15.7	°C/W
		TO-252-2				

1A3-TERMINAL POSITIVE LINEAR REGULATOR
7800
Electrical Characteristics (Continued)

7812 (Vi=19V, Io=500mA, TJ=-40 to 125 °C, unless otherwise specified.)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Output Voltage	VO	TJ=25°C	11.5	12	12.5	V
		Io=5mA to 1A, Vi=14.5 to 27V, PD ≤ 15W	11.4	12	12.6	
Line Regulation	VR _{LINE}	Vi=14.5 to 30V, TJ=25°C		8	240	mV
Load Regulation	VR _{LOAD}	Vi=19V, Io=5mA to 1.5A, TJ=25°C		6	240	mV
Quiescent Current	I _Q	Vi=19V		3.4	6.5	mA
Quiescent Current Change	ΔI _Q	Vi=14.5 to 30V, Io=500mA, TJ=25°			1	mA
		Io=5.0mA to 1A, TJ=25°C			0.5	
Ripple Rejection	ΔVi/ΔVo	Vi=15V to 25V, f=120Hz	55	60		dB
Dropout Voltage	V _{DROP}	ΔVo/Vo=1%, Io=1A, TA=25°C		2.0		V
Output Noise Voltage	NO	f=10Hz to 100KHz, TA=25°C		10		μV/Vo
Output Resistance	R _O	f=1.0KHz		13		mΩ
Short Circuit Current	I _{SC}	Vi=35V, TA=25°C		0.2		A
Peak Output Current	I _{PK}	Vi=17V, TJ=25°C		2.2		A
Output Voltage Drift	ΔVo/ΔT			0.8		mV/°C
Thermal Resistance	θ _{JC}	TO-220-3		8.8		°C/W
		TO-252-2		15.7		

1A3-TERMINAL POSITIVE LINEAR REGULATOR

7800

Typical Performance Characteristics

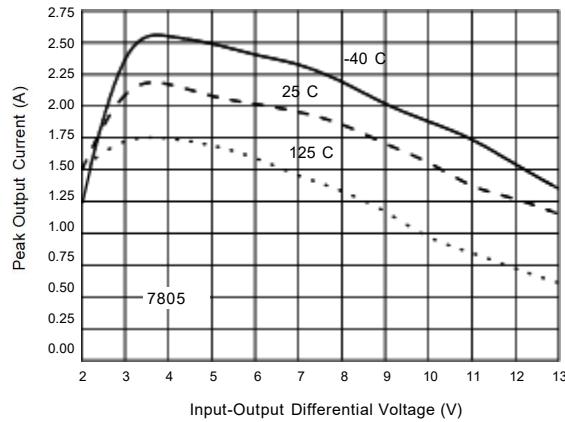


Figure 4. Peak Output Current vs. Input-Output Differential Voltage

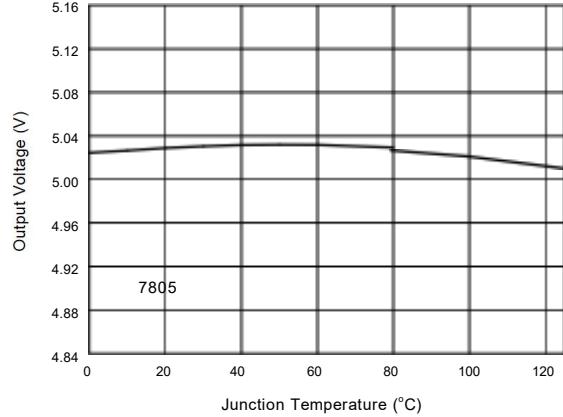


Figure 5. Output Voltage vs. Junction Temperature

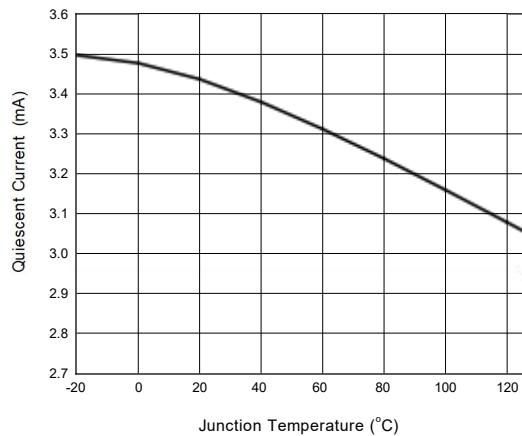


Figure 6. Quiescent Current vs. Junction Temperature

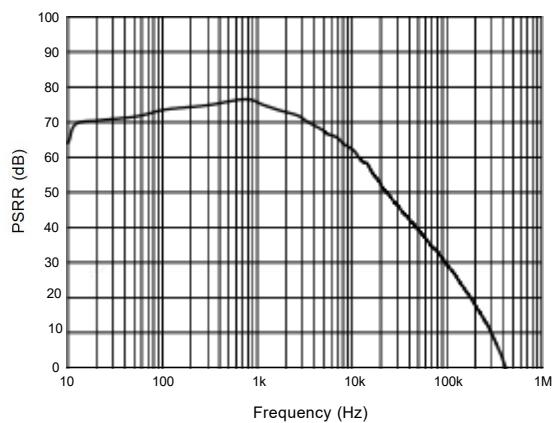


Figure 7. PSRR vs. Frequency

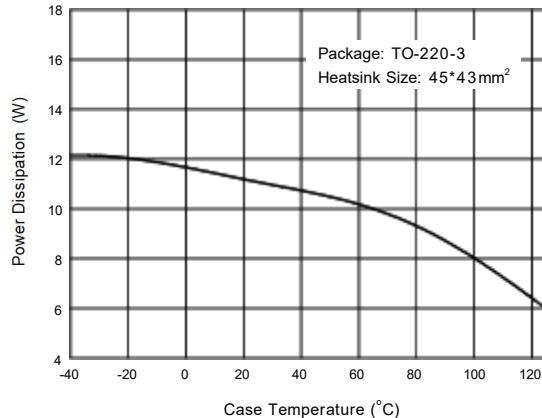
1A3-Terminal Positive Linear Regulator
7800
Typical Performance Characteristics (Continued)


Figure 8. Power Dissipation vs. Case Temperature

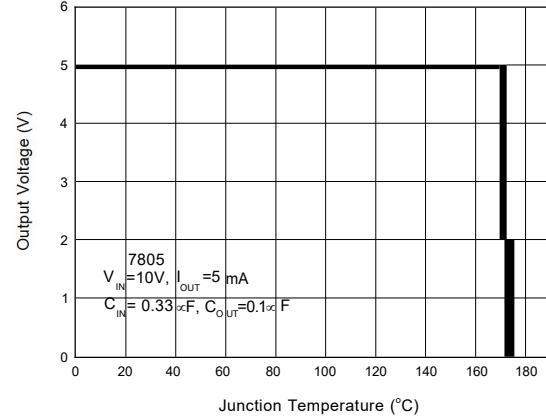


Figure 9. Thermal Shutdown Protection

1A3-Terminal Positive Linear Regulator

7800

Typical Application

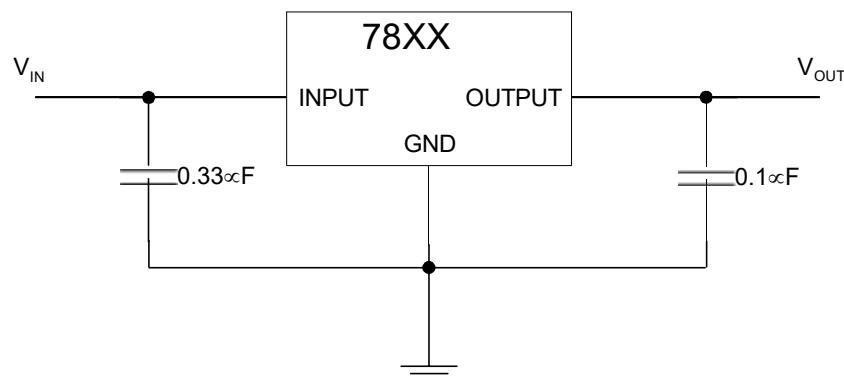
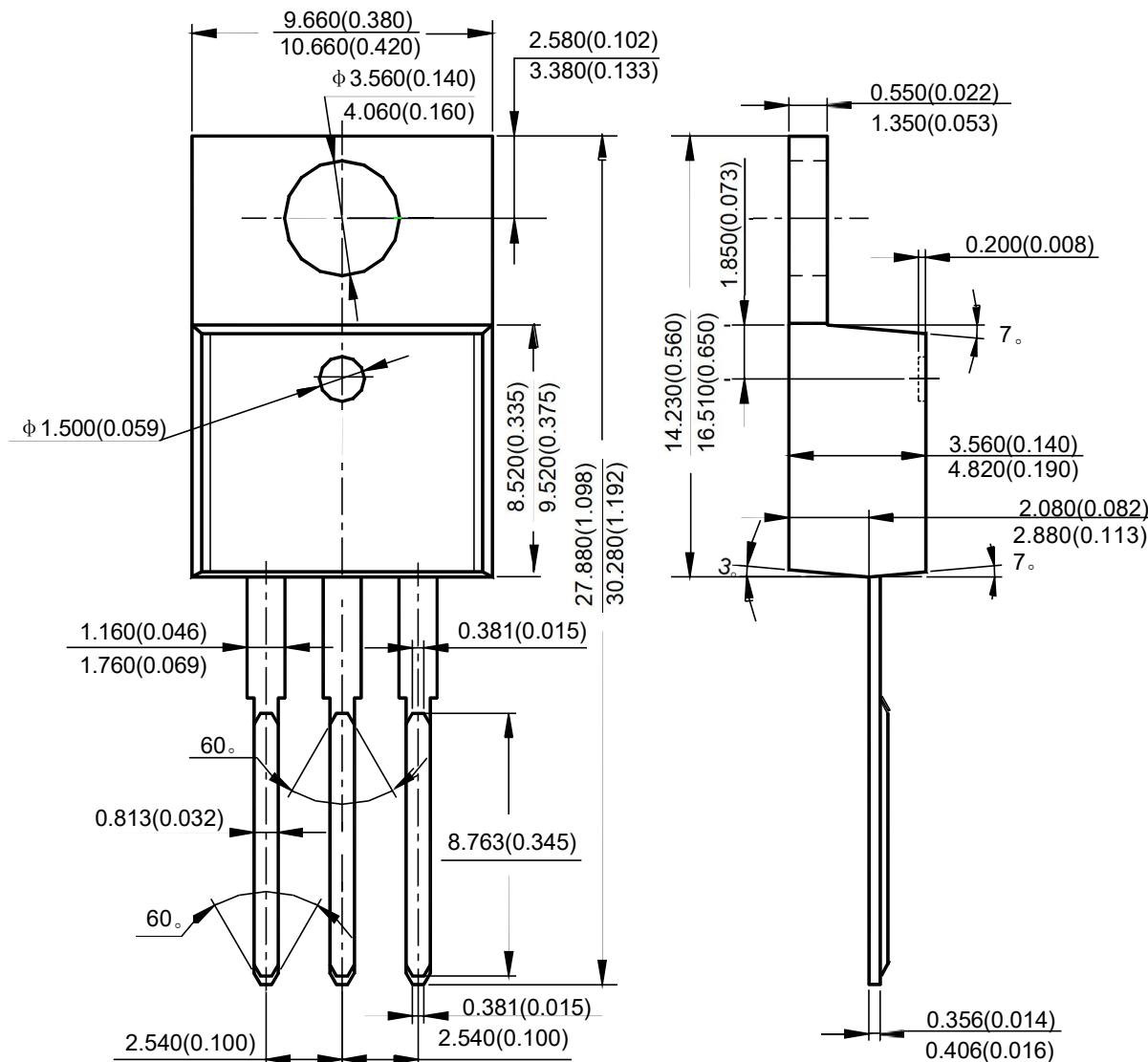
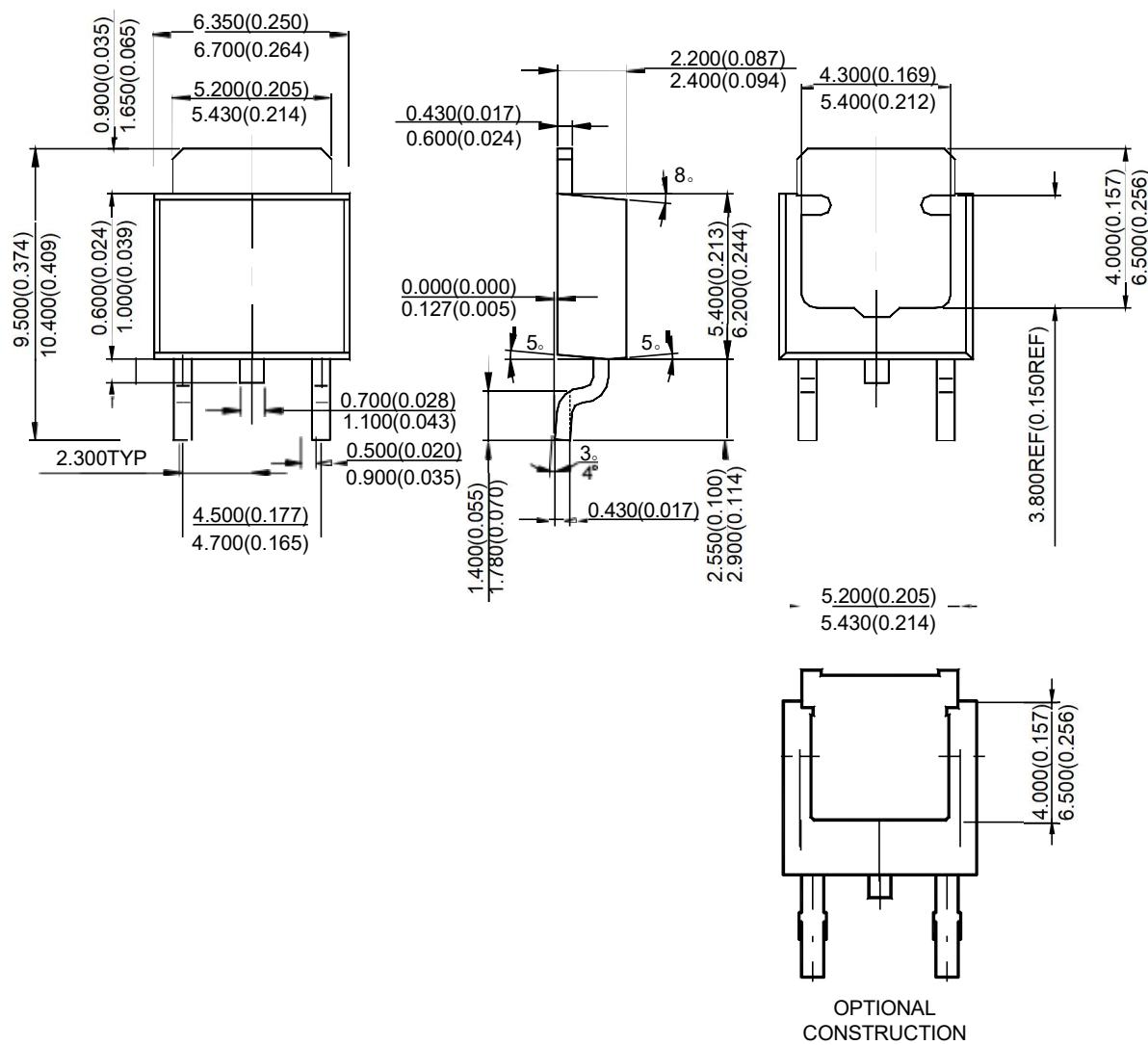


Figure 10. Typical Application of 7800

1A3-Terminal Positive Linear Regulator
7800
Mechanical Dimensions
TO-220-3
Unit: mm(inch)


1A3-Terminal Positive Linear Regulator
7800
Mechanical Dimensions (Continued)
TO-252-2
Unit: mm(inch)

Sales Office: